

JFW



PATENT  
Customer No. 22,852  
Attorney Docket No. 03180.0326

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Tadashi IGUCHI et al. ) Group Art Unit: 2826  
Application No.: 10/612,033 ) Examiner: Quinto, Kevin V.  
Filed: July 3, 2003 )  
For: SEMICONDUCTOR MEMORY ) Confirmation No.: 5852  
PREVENTING AN ELECTRIC )  
SHORT CIRCUIT BETWEEN A )  
WORK LINE AND A )  
SEMICONDUCTOR SUBSTRATE, )  
AND MANUFACTURING )  
METHOD FOR THE )  
SEMICONDUCTOR MEMORY (As )  
Amended) )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**SUPPLEMENTAL AMENDMENT**

Applicants thank the Examiner for the telephone conversation with Applicants' representative on September 29, 2004, during which the Examiner suggested amendments to the claims and stated that the application will be allowed if Applicants submit a Supplemental Amendment incorporating his proposed amendments. Accordingly, Applicants hereby submit this Supplemental Amendment, amending claims 1, 12 and 17. The claim listing reflecting the amendments to these claims begins on page 2 of this paper.